

Electronic Acknowledgement Receipt

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| EFS ID: | 1793656 |
| Application Number: | 10070091 |
| International Application Number: | |
| Confirmation Number: | 7309 |
| Title of Invention: | Register set used in multithreaded parallel processor architecture |
| First Named Inventor/Applicant Name: | Gilbert Wolrich |
| Customer Number: | 20985 |
| Filer: | Ido Rabinovitch/Debbie Nast |
| Filer Authorized By: | Ido Rabinovitch |
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| Submitted with Payment | yes |
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| RAM confirmation Number | 4866 |
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File Listing:

| Document Number | Document Description | File Name | File Size(Bytes) | Multi Part /.zip | Pages (if appl.) |
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| 1 | Notice of Appeal Filed | 10559310US1noticeofappeal.pdf | 28995 | no | 1 |
| Warnings: | | | | | |
| Information: | | | | | |
| 2 | Fee Worksheet (PTO-06) | fee-info.pdf | 8179 | no | 2 |
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